

Modelling silicon carbide based power electronics in electric vehicles as a study of the implementation of semiconductor devices using Dymola

Leonard Janczyk¹ Yoshihisa Nishigori² Yasuo Kanehira³

¹Dassault Systèmes, Germany, leonard.janczyk@3ds.com

²ROHM Co., Ltd., Japan, yoshihisa.nishigori@rohmdsn.co.jp

³Dassault Systèmes, Japan, yasuo.kanehira@3ds.com

Abstract

In a joint effort, Dassault Systèmes and Rohm Semiconductor demonstrate how the introduction of silicon carbide (SiC) as a base material in power electronics improves the energy efficiency of a typical electric vehicle. As an application example simulation models of an electric drive and an electric vehicle are chosen.

Keywords: Power electronics, inverter, electric vehicles, Silicon carbide semiconductor, Dymola, Electrified Powertrains Library

1 Introduction

With the evolution of electric vehicles, electric powertrains with electric motors powered by a battery are increasingly becoming part of vehicles as well as of the onboard power supply system. In the industry market, renewable energies have different specifications and must be converted to fit to existing power networks. Additionally, in many cases, renewable energy system need a battery to operate continuously (Nakamura, 2015).

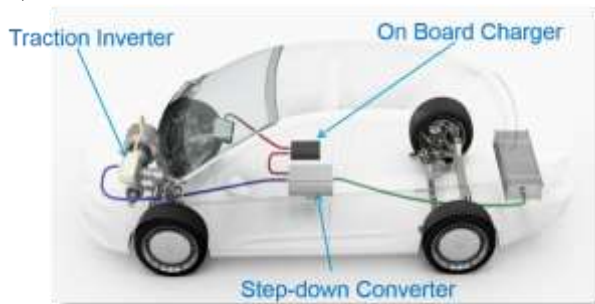


Figure 1 Concept drawing with power electronics linking the plug-in-charger (red), the DC battery circuit (green) the electric motor (blue).

In the mentioned applications, an electric system needs to link circuits with different voltage levels or circuits with alternating current (AC) and direct current (DC). This connection is facilitated by transformers or power electronics. Power electronics are often used in systems where dimensions and weight are important, such as electric vehicles (EV). For example, power electronics are required to link the battery pack, the electric machine and plug-in cable chargers, as illustrated in Figure 1.

Dassault Systèmes and Rohm Semiconductor are jointly investigating the use of system simulation for these cases, which utilizes semiconductor devices in a system simulation environment like Dymola.

After providing background information on power electronics and the advantages of SiC in chapter 2, the inverter models from the power electronics package in the Electrified Powertrains Library (EPTL) are introduced in chapter 3.

The thermal-electric modeling and calibration of an inverter module are explained in chapter 4.

In chapter 5, the inverter model is used in a system model of an electric drive to analyze the effect of the new power electronics material on the system electric efficiency and cooling requirements.

After converting the electric drive system model into a multi-dimensional table model in order to improve simulation speed, the improvement in energy efficiency and vehicle range is shown by a simulation of an electric vehicle.

2 Background

2.1 Silicon carbide (SiC)

Like most semiconductors, power electronics use silicon as a base material. Other types of materials include germanium and gallium nitride. A newcomer to this list is silicon carbide (SiC). Its material properties make it ideal for usage in power electronics: it is among the most mechanically robust materials (after carbon diamonds) and is able to withstand operating temperatures up to 200°C. Its specific thermal conductivity is five times higher than that of silicon, which facilitates the dissipation of electric loss power. Finally, the specific electric resistance is lower than traditional semiconductor materials.

SiC is a compound semiconductor which consists of silicon (Si) and carbon (C). It has superior performance compared to Si, with the breakdown electric field being ten times stronger and the band gap three times wider.

2.2 SiC characteristics in power electronics devices

Since the breakdown electric field of SiC is a magnitude stronger than Si, it is possible to build high-voltage devices with maximum operation voltages ranging from 600 V to a few thousand Volts with high doping density and thin drift layer. As most of the resistance of a high-voltage power device is in the drift layer, SiC can realize a high breakdown voltage device with extremely low on-resistance per unit area. In theory, the drift layer resistance per area can be reduced by a factor of 300 compared to Si for the same breakdown voltage (Nagano, 2018).

Minority carrier devices, i. e. bipolar devices such as IGBTs (Insulated Gate Bipolar Transistor) have been mainly used for Si in order to improve the increase in on-resistance accompanying high breakdown voltage. It has a problem on larger switching loss, however, the switching frequency is limited due to the dissipated heat generated by the electric loss power.

With SiC, a majority carrier device such as Schottky Barrier Diode (SBD) and MOSFET, which is a high-speed device structure, can be manufactured with high breakdown voltage. The device simultaneously enables three characteristics for power transistors: high breakdown voltage, low on-resistance, and high speed. Moreover, the wider band gap than Si allows power devices that can operate at very high temperatures (Tanaka, 2018) and (Ogawauchi, 2018).

Historically, Rohm financed the development of SiC devices and established an integrated production ranging from pulling wafer ingots to packaging and test. Rohm is starting to offer high-quality SiC devices, SBDs, and modules.

The adoption in industrial high-voltage power supplies and onboard electric vehicles chargers has started (Nakamura, 2015). Rohm is an official

technology partner of the Venturi Formula E team and provides SiC devices for motor drive inverters.

In this way, SiC is a key state-of-the-art device to increase the efficiency of electric power systems around the world.

3 Electrified Powertrains Library

3.1 Overview

The Electrified Powertrains Library (EPTL) contains models covering the key components of an electric drive system in different levels of detail, i.e. physical, switched, averaged and energy-based. The key components are electric machines and inverters with their respective controllers.

3.2 Power electronics package

The power electronics package contains models to describe switching electronic devices required in the context of e.g. electric drive modeling.

It contains the semiconductor switches at the heart of the power electronic devices as well as power electronics devices such as inverters and converters.

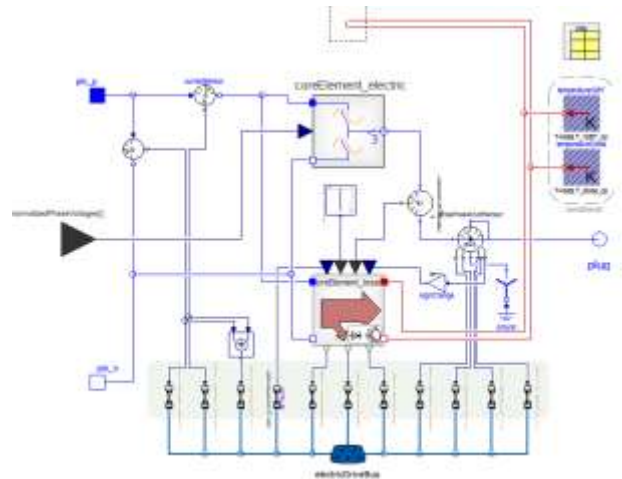


Figure 2 Electrical and loss model of an inverter.

In this context, inverters are of special interest, as their functionality allows to bridge AC and DC electric circuits. Inverter models are available in several levels of detail:

- idealized averaged electric models,
- averaged electric models with losses,
- idealized switching electrical models,
- switching electrical models with losses,

3.2.1 Electrical Model

Among the model variants available in the EPTL, the electrical behavior is modelled by the averaged model with constant efficiency in order to increase the simulation speed in large scale system models in later stages.

The input load signal is the desired three-phase voltage.

The active current that has to be drawn from the battery determined by recording the phase currents and phase shift. For the active current, the Clarke transformation is subsequently applied on the three-phase currents for converting the AC currents into a space vector with real and imaginary part.

Assuming a constant efficiency η , the relationship between the current on AC and DC side of the inverter is modelled by the following equation:

$$I_{DC} = I_{AC} \cdot \left(1 + \frac{1 - \eta}{\eta}\right)$$

Major differences between the averaged and the switching electrical models include that the averaged model is an approximation of a switched inverter and does not describe the harmonics of the AC electric signal apart from the fundamental wave. Hence, the behavior is different to a model of a switched inverter as soon as the load is not 100% sinusoidal anymore. For example, switched inverter models coupled detailed with the respective loss models can be used to find a trade-off between current ripple and inverter losses.

A detailed description of the electric behavior models for MOSFET, IGBT and switches can be found in (Denz, 2014). The mentioned publication comprises also a validation of the EPTL models using electric measurement data.

3.2.2 Losses Model

The loss model computes the power losses incurred by the following effects:

- Conduction losses of the diode with regards to forward voltage V_{f0} taken from the datasheet

$$P_{cond,diode} = \left(\frac{1}{2\pi} - m \cdot \cos\left(\frac{\phi}{8}\right)\right) \cdot V_{f0} \cdot \sqrt{2} \cdot I$$

- Conduction losses of the switching element with regards to drain-source voltage V_{ds0} , drain current and device temperature:

$$P_{cond,switch} = \left(\frac{1}{2\pi} + \frac{m \cdot \cos(\phi)}{8}\right) \cdot V_{ds0} \cdot \sqrt{2} \cdot I$$

- Reverse recovery of the diode with regards to forward voltage forward current, device temperature and forward voltage:

$$P_{loss,RR} = n_s \cdot E_{rr} \cdot \frac{\sqrt{2}}{\pi} \cdot \left(\frac{V}{V_{ref}}\right)^{K_v}$$

- Loss energy per switching operation with regards to current, device temperature and voltage to switch:

$$P_{switching} = n_s \cdot (E_{on} + E_{off}) \cdot \frac{\sqrt{2}}{\pi} \cdot \left(\frac{V}{V_{ref}}\right)^{K_v}$$

In this context m is the modulation index, I the load current, n_s the number of switches, V_{ref} the reference voltage for the datasheet parameters, K_v a coefficient for accounting the non-linear influence of the normalized voltage on the losses.

Forward voltage, drain-source voltage as well as the energies for on/off switching and reverse-recovery need

to be entered as parameter table as specified in the datasheet, as given in the Appendix.

The power losses are turned into heat flow and can be connected to the thermal model in order to predict the device temperature.

3.2.3 Thermal Model

Along with the model describing the electrical behavior and the loss generation, shown in Figure 2, a thermal representation of the respective inverter is comprised. The diode and the MOSFET are modelled as so called Foster elements. The Foster element is an electric equivalent circuit model which describes the transient thermal behavior of the device as concatenated RC circuits. In this context the thermal mass is treated as capacitor element and the thermal conductance as resistor element. The thermal conductance from junction to the casing of the module's switch and diode is given by the datasheet and multiplied by the number of elements, in that application with the number of the module's switches, which is six for this device.

4 Modelling and calibration of an Silicon carbide based inverter module

4.1 Topology

The inverter model is based on the half bridge module BSM120D12P2C005 by Rohm Semiconductor (Rohm, 2016) with the characteristics as listed in Table 1.

Table 1. Characteristics of the used device (SiC) and a Silicon (Si) based benchmark device.

Category	Device	Benchmark
Technology	SiC	Si
Type	MOSFET	IGBT
Maximum Voltage	1200 V	1700 V
Voltage Drain-Source	600 V	900 V
Maximum Current	120 A	150 A
Operating Temperature	150 °C	80 °C
Number of switches	6	6
Voltage coefficient K_v	1.35	1.35

It consists of a MOSFET and a Schottky Barrier Diode (SBD), both based on silicon carbide.

The module is modelled using an averaged electric model with losses and a thermal model

4.2 Calibration of the power module

Additionally to the scalar parameters as shown in Table 1, the electric model of the diode includes the multi-dimensional relationship of source current and source drain voltage with regards to the operating temperature of the device.

Moreover, the calculation of the dissipated heat losses requires to enter the loss energy per switching operation as well as the recovered loss energies with regards to drain current and operation temperature.

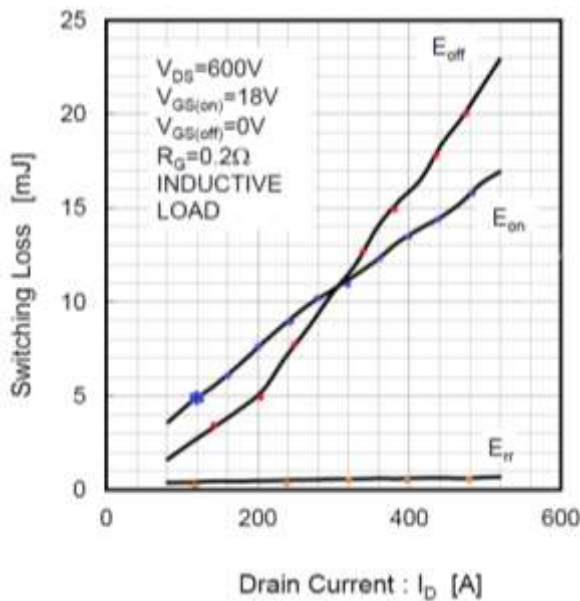


Figure 3. Sampling the characteristic curve of the used SiC device for switching losses when operating at 25°C.

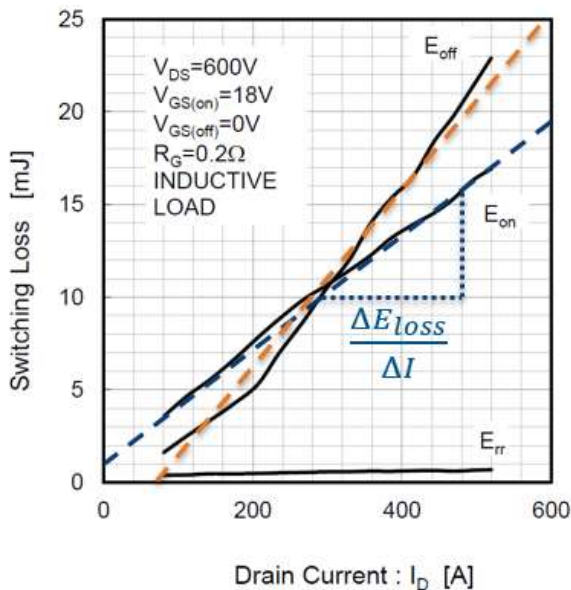


Figure 4. Linearizing the characteristic curve of the used SiC device for switching losses when operating at 25°C.

There are two options for calibrating the SiC device in the EPTL:

(1) Table Based: Either by sampling the characteristic curves given for the respective device, as indicated for example in Figure 3. The recorded data set is interfacing the Modelica model via the `NDTable` class. The output for given operating load and conditions is generated by performing a multi-dimensional interpolation in the data set.

(2) Linear equation: When the device characteristics feature sufficiently linear dependencies, the curve can be captured by linear equations within a confined area of operation as sketched in Figure 4.

With the switch-off loss energy curve with regards to drain current displays discontinuities, especially at 200 amperes, the table based method is chosen for modelling the losses.

The class `NDTable` for is implemented as described by (Schmitt et al, 2015) for an example for the forward characteristic of a Si-based diode.

4.3 Testbench simulation

In order to assess the impact of the increased efficiency the inverter is simulated as part of an electric drive system model as shown in Figure 5.

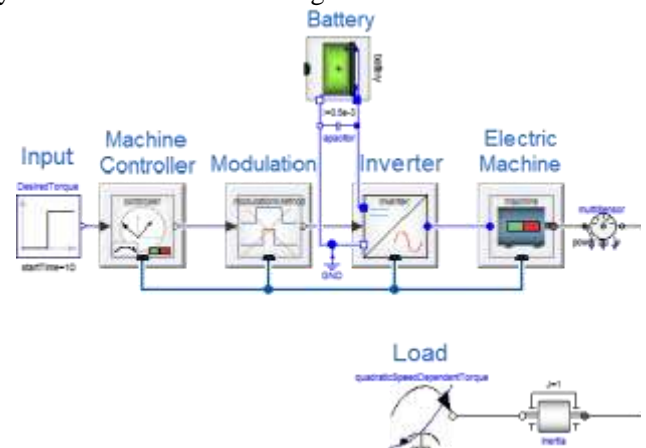


Figure 5 Simulation set up: Electric drive system including electric and loss model of the inverter comprising the power electronics.

The electric drive consists of five components: the electric machine with its machine controller, the inverter with modulation and the battery pack. The impedance effects of the cables of the high-voltage circuit as well as additional electric consumers of auxiliary devices are neglected in this setup.

Table 2 Simulation starting values and boundary conditions for the simulation.

Variable [Unit]	Value
Operating Temperature [°C]	80
Switching frequency [kHz]	25
Modulation harmonic [type]	sinus
Electric machine type [type]	PSM
Nominal machine frequency [Hz]	195
Nominal torque [Nm]	122
Nominal phase RMS voltage [V]	114
Nominal RMS current per phase [A]	170
Electric machine power [kW]	50
Desired Torque as input [Nm]	+10
Load Torque [Nm]	-10
Nominal machine speed [rpm]	1000

The electric machine is a 50 kW three-phase permanent magnet synchronous machine (PSM) with three pairs of poles. The corresponding machine controller has the target torque as input and features field-weakening control and the maximum-torque-per-ampere control.

With 25 kHz, a comparably high value has been chosen as switching frequency in order to illustrate the contrast in terms of dissipated loss power.

The overall boundary conditions of the simulation are laid down in Table 2.

The simulation in Figure 6 illustrates that lower switching energies result into lower heat loss power, hence a lower power consumption on the DC power side.

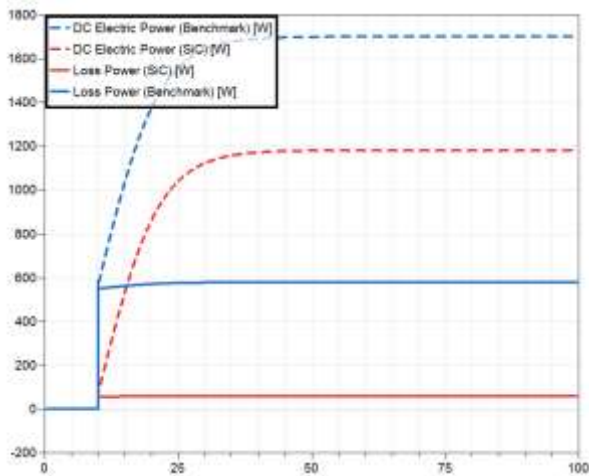


Figure 6 Comparison of DC load power and loss power between Si-based benchmark device (blue) and SiC-based device (red) for the first 100 seconds simulation time.

With the DC power load reduced significantly, the battery experiences a lower draining for providing a similar level of AC output. The battery charging state (SOC, that is the state-of-charge) therefore remains at a higher level.

5 Simulation and discussion

5.1 Water-cooled electric drive system

In order to assess the implication of the reduced loss power as calculated in chapter 4.3, the electric testbench component models are complemented by the respective thermal representation.

The use case as illustrated in Figure 7 consists of a permanent magnet synchronous machine which is water-cooled with the used liquid cooling media type being water/ethylene-glycol (50:50), as given in the FluidHeatFlow package in the Thermal library of the Modelica Standard Library 3.2.2. It represents a typical automotive setup with an external pump causing a volume flow through the inverter first and the machine subsequently. The inlet and outlet of the cooling cycle are modelled by the class Sources.Ambient. The

component pump is an instance of the VolumeFlow class in the same package.

As modelling complexity increased significantly with the liquid cooling cycle, the battery was replaced by voltage source.

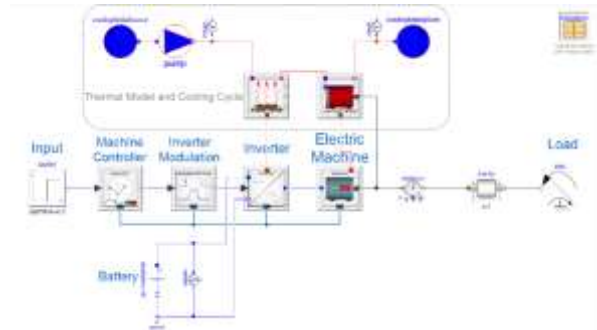


Figure 7 Simulation set up: Water-cooled electric drive system including thermal-electric models of inverter and electric machine.

The resulting reduction of the load for the cooling cycle is assessed by a simulation with the configuration as laid down in Table 3.

Table 3 Simulation starting values and boundary conditions for the simulation.

Variable [Unit]	Value
Inlet temperature coolant [°C]	65
Volume flow [l/min]	6.0
Operating Temperature [°C]	transient
Switching frequency [kHz]	25
Modulation harmonic [type]	sinus
Electric machine type [type]	PSM
Electric machine power [kW]	50
Desired Torque (Input) [Nm]	+122
Load Torque [Nm]	-122
Nominal machine speed [rpm]	3000

In Figure 8 is result of the testbench simulation is shown. With the generated heat losses reduced, the coolant experiences a significantly lower temperature increase.

With using a cooling cycle with liquid cooling, the indicator by choice is the temperature of the cooling medium. The coolant's temperature is increased by the dissipated heat loss power. The interfacing thermal conductance model is provided by Foster circuit parameters.

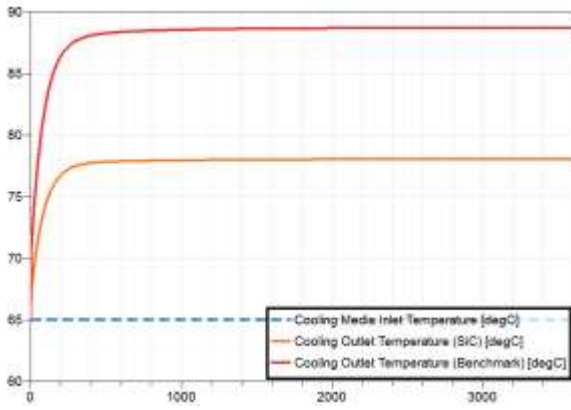


Figure 8 Simulation result: cooling medium temperature at the inlet and the outlet of the cooling cycle.

5.2 Electric vehicle simulation

The vehicle simulation package allows setting up example use cases for electric drives in different characteristics, providing an expandable and configurable infrastructure as well as all required components from environment to operational control models.

Hence characteristic maps are created for a specific supply voltage and specific temperatures. These values must be defined before the map generation process is started. For this purpose the boundary conditions as described in chapter 4.3 are chosen assuming that the inverter are operated in thermal equilibrium by using the cooling cycle as defined in chapter 5.1.

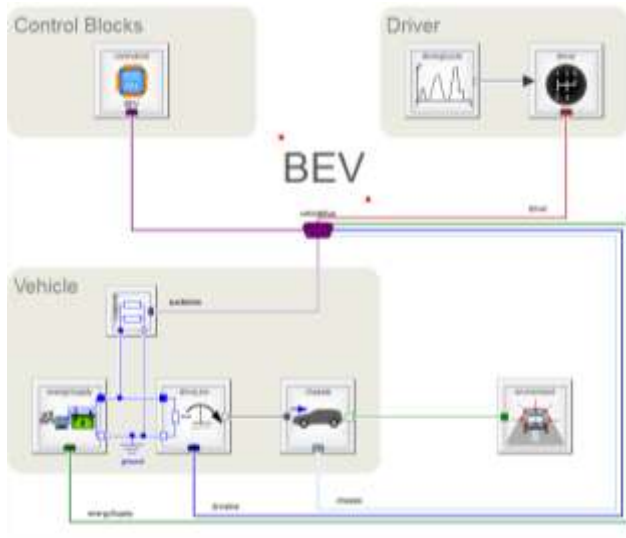


Figure 9 Electric Vehicle Simulation Testbench

With the battery being the primary source of energy, the electric powertrain as shown in Figure 10, the driving range increases significantly according to the simulation displayed in Figure 11.



Figure 10 Charging state (SOC) of the EV traction battery when adopting the driving profile “Artemis Motorway”.

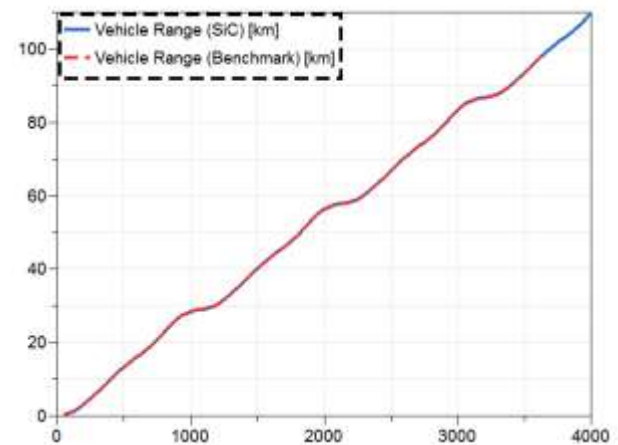


Figure 11 Maximum EV driving range when adopting the driving profile “Artemis Motorway”.

6 Outlook

Tuning, increased accuracy in the simulation environment still be necessary, but it was already possible to demonstrate the improvement generated by device replacement just with high-level simulation.

Application of SiC in DC/DC converters is currently increasing, and with improvements in the relationship of performance and cost, the authors are confident that the application of SiC will expand.

References

- Patrick Denz, Thomas Schmitt, Markus Andres. Behavioral Modeling of Power Semiconductors in Modelica. *Proceedings of the 10th International Modelica Conference*, Lund, Sweden, 2014. doi:10.3384/ECP14096343
- Rohm Co., Ltd. SiC Power Module BSM120D12P2C005. *Data sheet*. 2016.09 – Rev C. 2016.
- Thomas Schmitt, Markus Andres, Stephan Ziegler, Stephan Diehl. A Novel Proposal on how to Parameterize Models in Dymola Utilizing External Files under Consideration of a Subsequent Model Export using the Functional Mock-Up Interface. *Proceedings of the 11th International Modelica Conference*, Versailles, France, 2015. doi:10.3384/ecp1511823
- T. Nakamura, Y. Nakano, T. Hanada. Advanced SiC Power Devices and Their Prospects. 4_S10_5, VOL. 3, *IEE-Japan Industry Applications Society Conference*, Japan, 2015.
- K. Tanaka, S. Kyogoku, R. Iijima, S. Harada. Impact of the trench bottom shielding region on switching characteristics in SiC Double-trench MOSFETs. 4-010 *Annual Meeting of the IEEJ* 2018.
- T. Nagano, K. Matsubara, H. Takubo, A. Toba. Suppression of Surge Voltage by Active Gate Driving for SiC-MOSFETs. 4-017, *Annual Meeting of the IEEJ* 2018.
- Y. Ogawauchi, K. Nakahara “A Study on High-speed Gate Drive Circuit Using Hybrid Current/Voltage Source of SiC-MOSFET” 4-018 *Annual Meeting of the IEEJ* 2018

Appendix

Rohm BSM120D12P2C005 parameters

Output Characteristics

MOSFET drain-source voltage with regards to drain current and device temperature, measured in Volt.

ID [A]	25°C	125°C	150°C
40	0.6507	0.9771	1.105
80	1.3582	1.9828	2.224
120	2.1082	3.0735	3.457
160	2.9151	4.2496	4.818
200	3.8213	5.6101	6.377
240	4.8	7.1269	8.500

Diode Forward Characteristics

Source-drain voltage with regards to source current and device temperature, measured in Volt.

I [A]	25°C	125°C	150°C
30	0.53	0.80	0.83
40	0.70	0.92	0.95
60	1.00	1.20	1.75
80	1.13	1.30	1.40
120	1.35	1.65	1.80
160	1.55	2.00	2.20
200	1.75	2.40	2.60
240	1.97	2.75	3.05

Switch-on energy

Loss energy per switch-on operation with regards to load current and device temperature, measured in Joule.

I [A]	25°C	125°C
20	0.8000e-3	1.0000e-3
40	1.4946e-3	1.3441e-3
80	2.7321e-3	2.2566e-3
120	3.9696e-3	3.1816e-3
160	5.2071e-3	4.0440e-3
200	6.5571e-3	5.0066e-3
240	8.2607e-3	5.9441e-3

Switch-off energy

Loss energy per switch-off operation with regards to load current and device temperature, measured in Joule.

I [A]	25°C	125°C
20	0.3000e-3	0.3000e-3
40	0.4982e-3	0.5809e-3
80	1.1732e-3	1.2807e-3
120	1.9445e-3	2.1932e-3
160	2.8125e-3	3.1432e-3
200	3.8893e-3	4.3435e-3
240	5.0143e-3	5.6813e-3

Reverse Recovery

Reverse recovery energy per switching operation with regards to load current and device temperature, measured in Joule.

I [A]	25°C	125°C
20	0.0482e-3	0.0934e-3
40	0.0321e-3	0.0801e-3
80	0.0161e-3	0.0792e-3
120	0.0161e-3	0.0659e-3
160	0.0161e-3	0.0300e-3
200	0.0161e-3	0.0300e-3
240	0.0161e-3	0.0300e-3

Silicon benchmark device parameters

Output Characteristics

IGBT collector-emitter voltage with regards to collector current and device temperature, measured in Volt.

I[A]	25°C	125°C	150°C
40	1.31	1.34	1.33
80	1.56	1.75	1.78
120	1.83	2.1	2.18
160	2.04	2.43	2.54
200	2.25	2.76	2.92
240	2.47	3.1	3.31

Diode Forward Characteristics

Drain-source voltage with regards to source current and device temperature, measured in Volt.

I	25°C	125°C	150°C
40	1.17	1.06	1.06
80	1.39	1.35	1.35
120	1.54	1.55	1.56
160	1.68	1.74	1.77
200	1.81	1.9	1.95
240	1.92	2.06	2.12

Switch on Energy

Loss energy per switch-off operation with regards to load current and device temperature, measured in Joule.

I [A]	125°C	150°C
40	0.018	0.020
80	0.032	0.035
120	0.046	0.050
160	0.061	0.070
200	0.076	0.080
240	0.093	0.100

Switch off Energy

Loss energy per switch-off operation with regards to load current and device temperature, measured in Joule.

I [A]	125°C	150°C
40	0.013	0.015
80	0.025	0.028
120	0.036	0.040
160	0.045	0.050
200	0.054	0.060
240	0.063	0.069

Reverse Recovery

Reverse recovery energy per switching operation with regards to load current and device temperature, measured in Joule.

I [A]	125°C	150°C
40	0.019	0.023
80	0.028	0.031
120	0.034	0.038
160	0.040	0.044
200	0.045	0.048
240	0.048	0.051